

Complementary Enhancement Mode MOSFET

1. Product Information

1.1 Features

- ☒ Surface-mounted package
- ☒ Advanced trench cell design

1.2 Applications

- ☒ MB and NB
- ☒ Motor drivers
- ☒ Half – bridge Drivers

1.3 Quick reference

N-channel

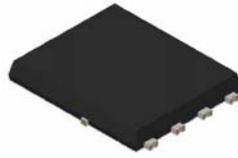
- ☒ $BV \leq 30\text{ V}$
- ☒ $P_{\text{tot}} \leq 19\text{ W}$
- ☒ $I_D \leq 39\text{ A}$
- ☒ $R_{\text{DS(ON)}} \leq 7.5\text{ m}\Omega$ @ $V_{\text{GS}} = 10\text{ V}$
- $R_{\text{DS(ON)}} \leq 10.5\text{ m}\Omega$ @ $V_{\text{GS}} = 4.5\text{ V}$

P-channel

- ☒ $BV \leq -30\text{ V}$
- ☒ $P_{\text{tot}} \leq 19\text{ W}$
- ☒ $I_D \leq -22\text{ A}$
- ☒ $R_{\text{DS(ON)}} \leq 25\text{ m}\Omega$ @ $V_{\text{GS}} = -10\text{ V}$
- $R_{\text{DS(ON)}} \leq 42\text{ m}\Omega$ @ $V_{\text{GS}} = -4.5\text{ V}$

2. Pin Description

Pin	Description	Simplified Outline	Symbol
1	Source(S1)	Top View PDFN5x6-8L	
2	Gate(G1)		
3	Source(S2)		
4	Gate(G2)		
5,6	Drain(D2)		
7,8	Drain(D1)		



3. Limiting Values

Symbol	Parameter	Conditions	Min	Max	Unit
N-channel					
V_{DS}	Drain-Source Voltage	$T_C = 25\text{ }^{\circ}\text{C}$	-	30	V
V_{GS}	Gate-Source Voltage	$T_C = 25\text{ }^{\circ}\text{C}$	-	± 20	V
I_D^*	Drain Current	$T_C = 25\text{ }^{\circ}\text{C}, V_{GS} = 10\text{ V}$	-	39	A
		$T_C = 100\text{ }^{\circ}\text{C}, V_{GS} = 10\text{ V}$	-	25	A
		$T_A = 25\text{ }^{\circ}\text{C}, V_{GS} = 10\text{ V}$	-	15	A
		$T_A = 70\text{ }^{\circ}\text{C}, V_{GS} = 10\text{ V}$	-	11	A
$I_{DM}^{*,****}$	Pulsed Drain Current	$T_C = 25\text{ }^{\circ}\text{C}, V_{GS} = 10\text{ V}$	-	120	A
P-channel					
V_{DS}	Drain-Source Voltage	$T_C = 25\text{ }^{\circ}\text{C}$	-	- 30	V
V_{GS}	Gate-Source Voltage	$T_C = 25\text{ }^{\circ}\text{C}$	-	± 20	V
I_D^*	Drain Current	$T_C = 25\text{ }^{\circ}\text{C}, V_{GS} = - 10\text{ V}$	-	- 22	A
		$T_C = 100\text{ }^{\circ}\text{C}, V_{GS} = - 10\text{ V}$	-	- 14	A
		$T_A = 25\text{ }^{\circ}\text{C}, V_{GS} = - 10\text{ V}$	-	- 8.4	A
		$T_A = 70\text{ }^{\circ}\text{C}, V_{GS} = - 10\text{ V}$	-	- 6.7	A
I_{DM}^{***}	Pulsed Drain Current	$T_C = 25\text{ }^{\circ}\text{C}, V_{GS} = - 10\text{ V}$	-	- 88	A
P_{tot}	Total Power Dissipation	$T_C = 25\text{ }^{\circ}\text{C}$	-	19	W
T_{stg}	Storage Temperature		- 55	150	$^{\circ}\text{C}$
T_J	Junction Temperature		- 55	150	$^{\circ}\text{C}$
$R_{\theta JA}^*$	Thermal Resistance- Junction to Ambient		-	45.5	$^{\circ}\text{C} / \text{W}$
$R_{\theta JC}^*$	Thermal Resistance- Junction to Case		-	6.5	$^{\circ}\text{C} / \text{W}$

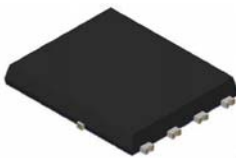
Notes :

- * Surface Mounted on 1 in² pad area, $t \leq 10\text{ sec}$
- ** Pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$
- *** limited by bonding wire

4. Marking Information

Product Name	Marking
iM1706	iM1706 XXXXX X : Date Code

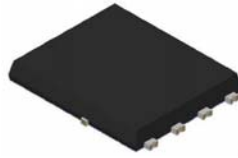
iM1706



5. Ordering Code

iM1706	Assembly Material: Halogen and Lead Free Device
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Note: inergy defines “ Green ” as lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900 ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500 ppm by weight; Follow IEC 61249-2-21 and IPC / JEDEC J-STD-020C)



6. Electrical Characteristics (T_A = 25 °C Unless Otherwise Noted)

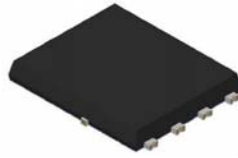
N-channel:

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250 μA	30	-	-	V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _{DS} = 250 μA	1	-	2.5	V
I _{DSS}	Zero Gate Voltage Source Current	V _{DS} = 24 V, V _{GS} = 0 V	-	-	1	μA
I _{GSS}	Gate Leakage Current	V _{GS} = ± 20 V, V _{DS} = 0 V	-	-	± 100	nA
R _{DS(ON)} ^a	Drain-Source On-State Resistance	V _{GS} = 10 V, I _D =20 A	-	6	7.5	mΩ
		V _{GS} = 4.5 V, I _D = 10 A	-	8	10.5	
Diode Characteristics						
V _{SD} ^a	Diode Forward Voltage	I _{SD} = 20 A, V _{GS} = 0 V	-	-	1.3	V
t _{rr}	Reverse Recovery Time	I _{SD} = 20 A, dI _{SD} /dt = 100 A/μs	-	17	-	nS
Q _{rr}	Reverse Recovery Charge		-	9.8	-	nC
Dynamic Characteristics ^b						
C _{iss}	Input Capacitance	V _{GS} = 0 V, V _{DS} = 15 V Frequency = 1 MHz	-	2596	-	pF
C _{oss}	Output Capacitance		-	217	-	
C _{rss}	Reverse Transfer Capacitance		-	170	-	
t _{d(on)}	Turn-on Delay Time	V _{DS} = 15 V, V _{GEN} = 10 V, R _G = 4.5 Ω, R _L = 1.75Ω, I _D = 20 A	-	9	-	nS
t _r	Turn-on Rise Time		-	54	-	
t _{d(off)}	Turn-off Delay Time		-	37	-	
t _f	Turn-off Fall Time		-	23	-	
Gate Charge Characteristics ^b						
Q _g	Total Gate Charge	V _{GS} = 10 V, V _{DS} = 15 V, I _{DS} = 20 A	-	47	-	nC
Q _{gs}	Gate-Source Charge		-	10	-	
Q _{gd}	Gate-Drain Charge		-	7.5	-	

Notes :

a : Pulse test ; pulse width ≤ 300 μs, duty cycle ≤ 2 %

b : Guaranteed by design, not subject to production testing



7. Electrical Characteristics (T_A = 25 °C Unless Otherwise Noted)

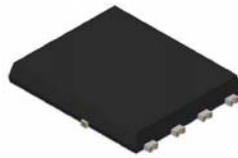
P-channel:

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = -250 μA	-30	-	-	V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _{DS} = -250 μA	-1	-	-2.5	V
I _{DSS}	Zero Gate Voltage Source Current	V _{DS} = -24 V, V _{GS} = 0 V	-	-	-1	μA
I _{GSS}	Gate Leakage Current	V _{GS} = ± 20 V, V _{DS} = 0 V	-	-	± 100	nA
R _{DS(ON)} ^a	Drain-Source On-State Resistance	V _{GS} = -10 V, I _D = -20 A	-	20	25	mΩ
		V _{GS} = -4.5 V, I _D = -10 A	-	32	42	
Diode Characteristics						
V _{SD} ^a	Diode Forward Voltage	I _{SD} = -20 A, V _{GS} = 0 V	-	-	-1.3	V
t _{rr}	Reverse Recovery Time	I _{SD} = -20 A, dI _{SD} /dt = 100 A/μs	-	17	-	nS
Q _{rr}	Reverse Recovery Charge		-	10	-	nC
Dynamic Characteristics ^b						
C _{iss}	Input Capacitance	V _{GS} = 0 V, V _{DS} = -15 V Frequency = 1 MHz	-	1838	-	pF
C _{oss}	Output Capacitance		-	174	-	
C _{rss}	Reverse Transfer Capacitance		-	136	-	
t _{d(on)}	Turn-on Delay Time	V _{DS} = -15 V, V _{GEN} = -10 V, R _G = 4.5 Ω, R _L = 0.75 Ω, I _D = -20 A	-	25	-	nS
t _r	Turn-on Rise Time		-	96	-	
t _{d(off)}	Turn-off Delay Time		-	211	-	
t _f	Turn-off Fall Time		-	121	-	
Gate Charge Characteristics ^b						
Q _g	Total Gate Charge	V _{GS} = - 10 V, V _{DS} = -15 V, I _{DS} = - 20 A	-	31	-	nC
Q _{gs}	Gate-Source Charge		-	8.7	-	
Q _{gd}	Gate-Drain Charge		-	5	-	

Notes :

a : Pulse test ; pulse width ≤ 300 μs, duty cycle ≤ 2 %

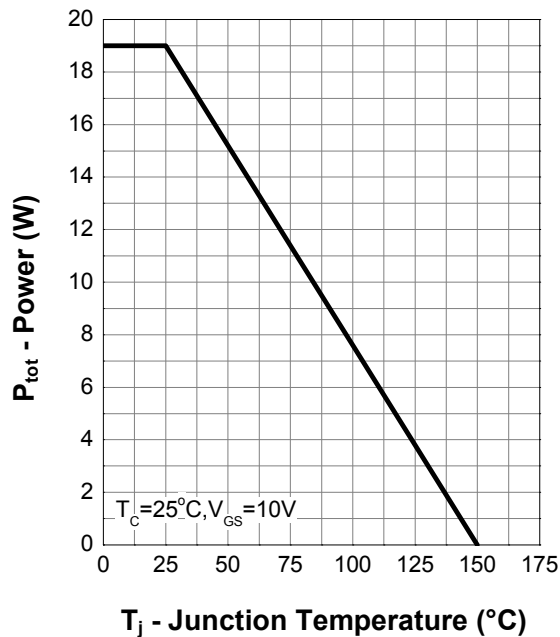
b : Guaranteed by design, not subject to production testing



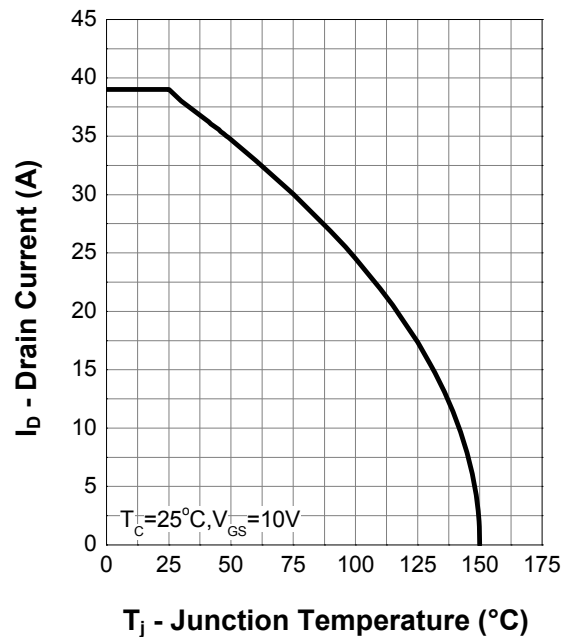
8. Typical Characteristics (Cont.)

N-channel:

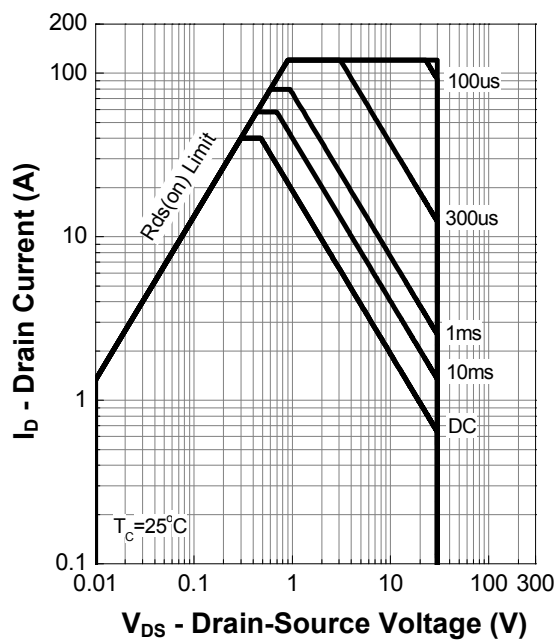
Power Capability



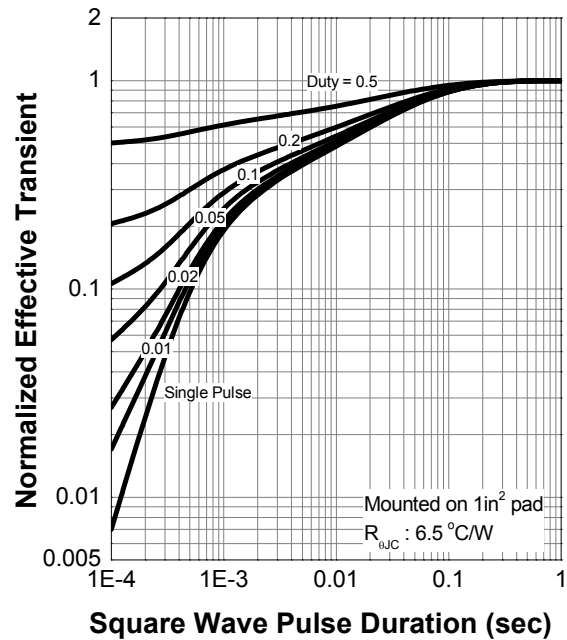
Current Capability

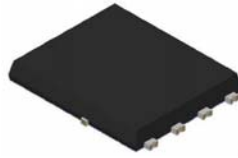


Safe Operation Area



Transient Thermal Impedance

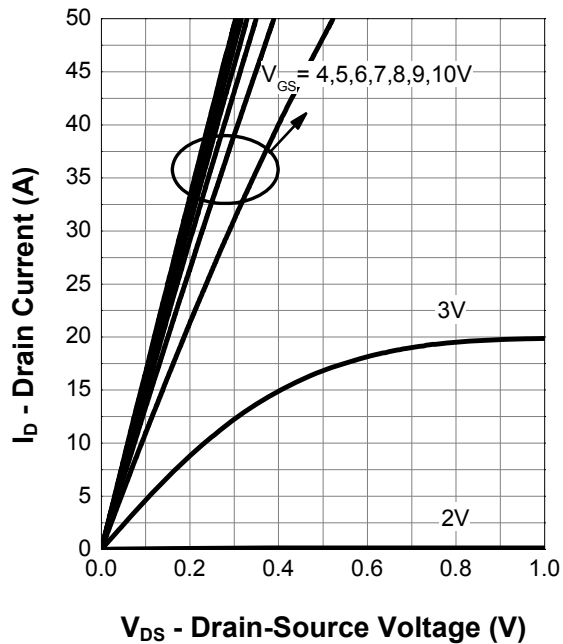




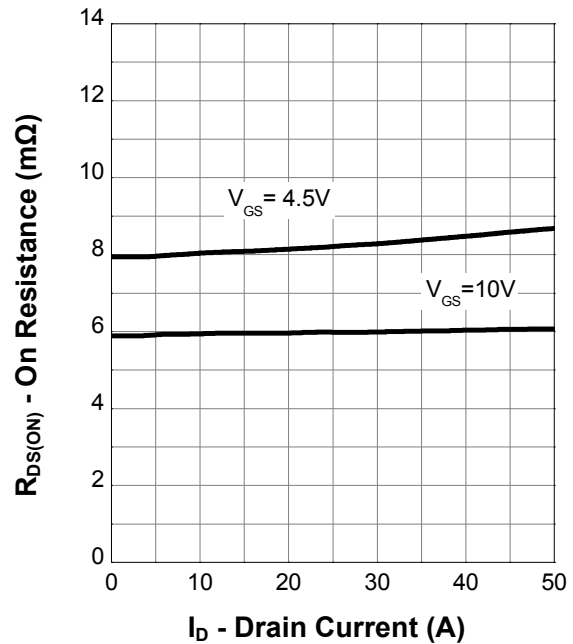
8. Typical Characteristics (Cont.)

N-channel:

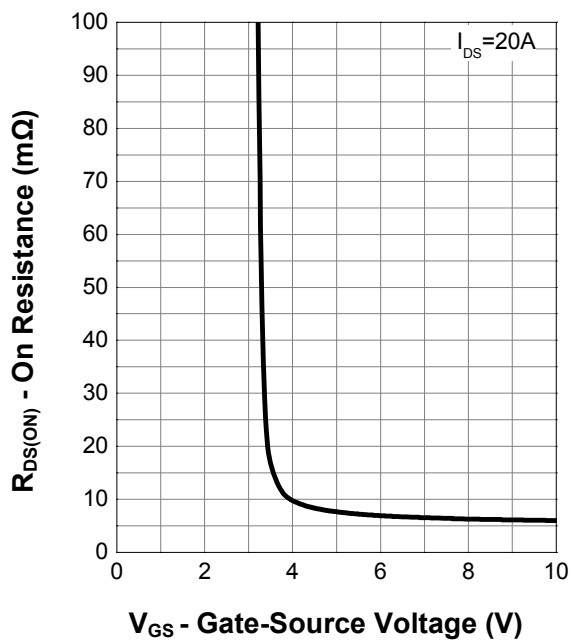
Output Characteristics



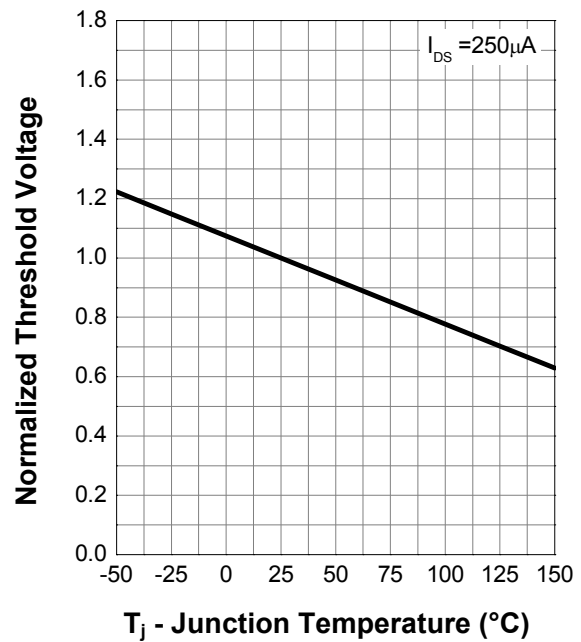
On Resistance

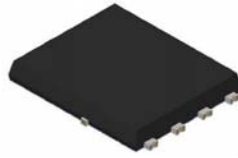


Transfer Characteristics



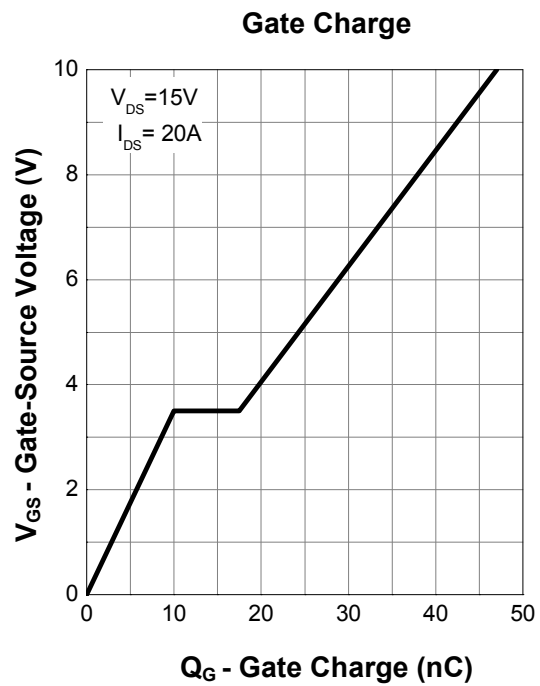
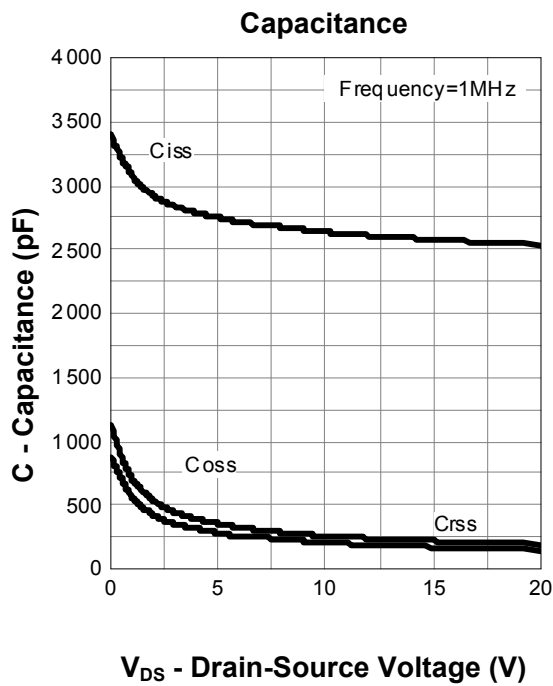
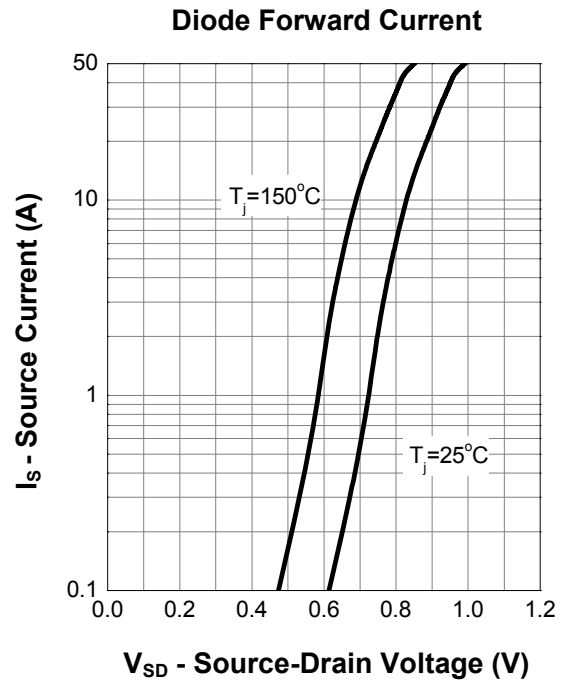
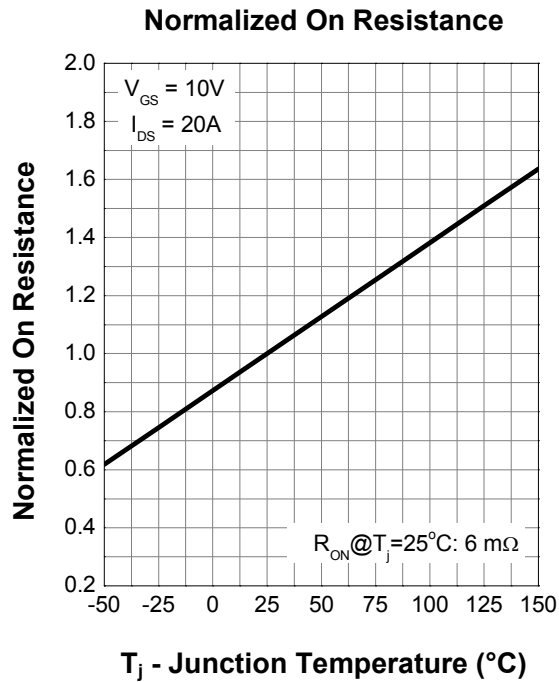
Normalized Threshold Voltage

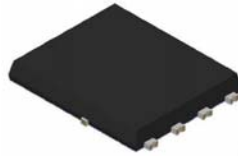




8. Typical Characteristics (Cont.)

N-channel:

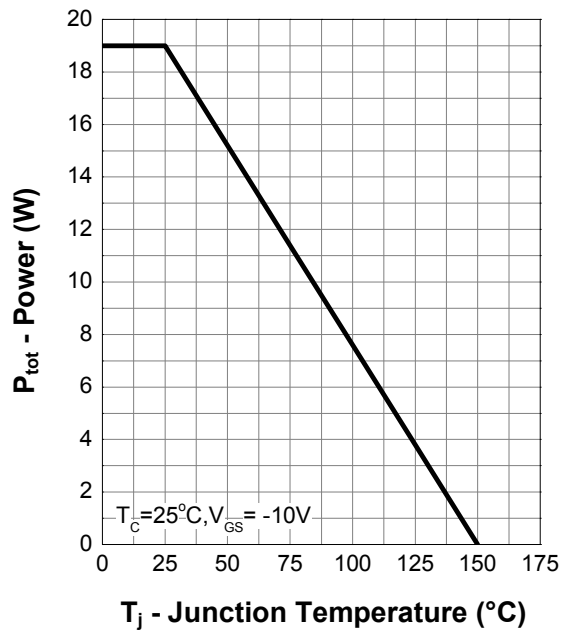




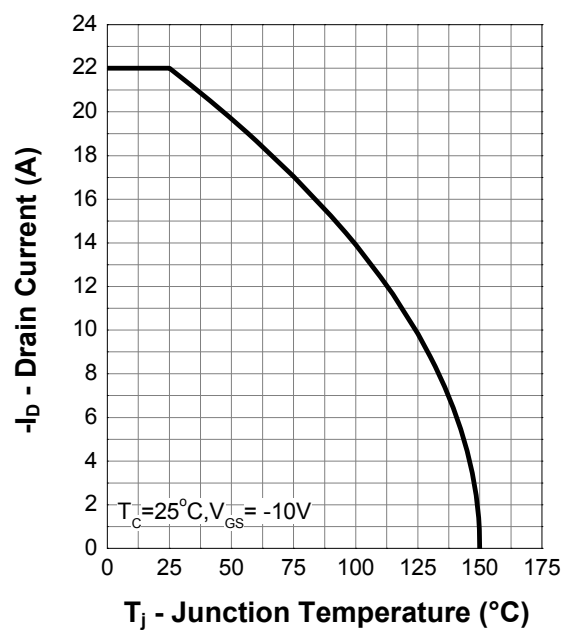
8. Typical Characteristics (Cont.)

P-channel:

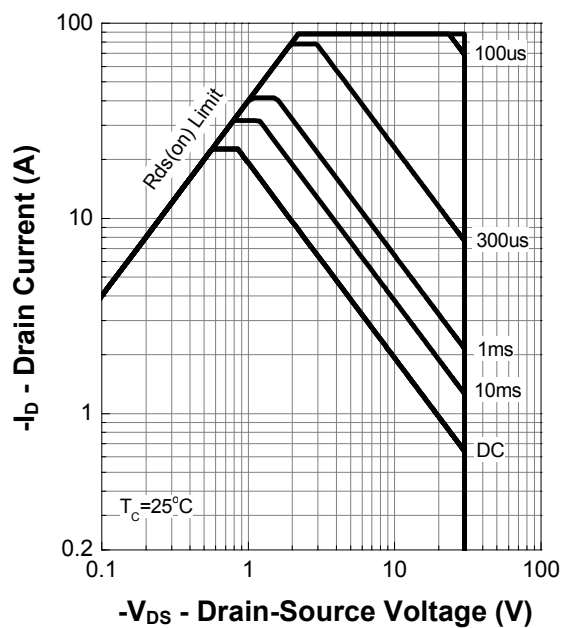
Power Capability



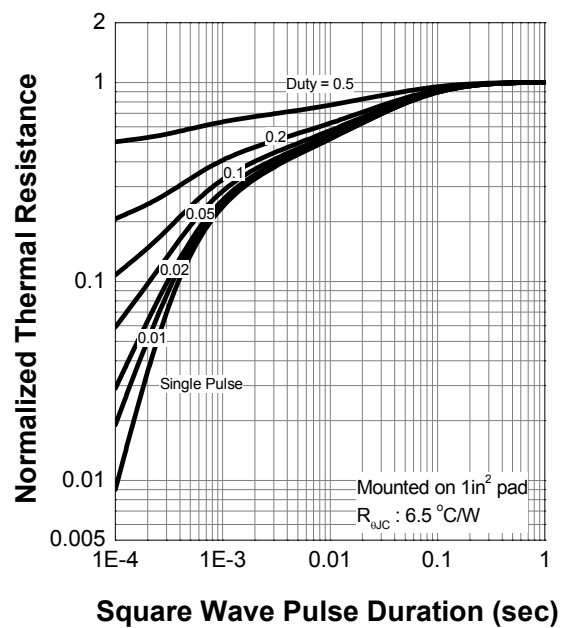
Current Capability

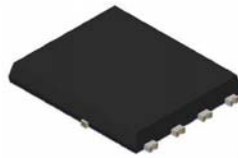


Safe Operation Area



Transient Thermal Impedance

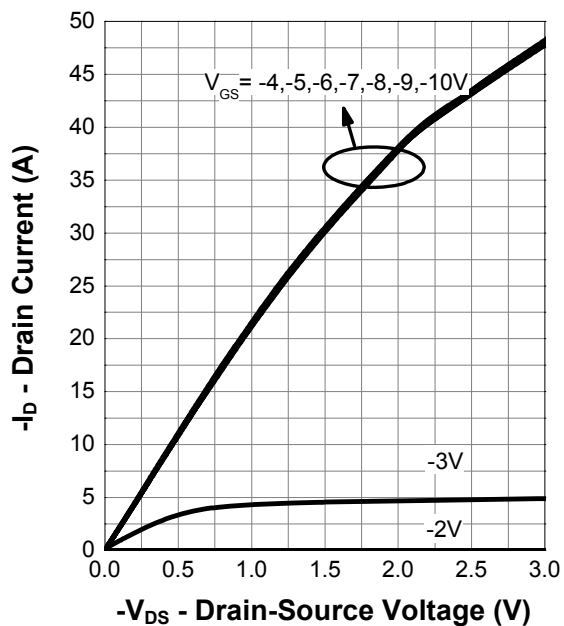




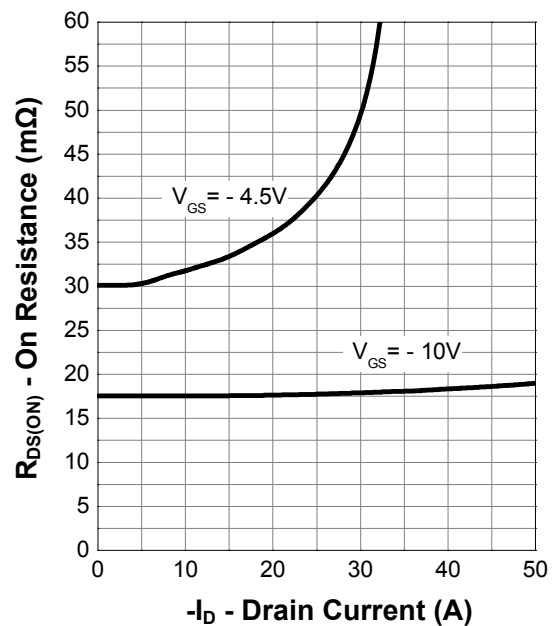
8. Typical Characteristics (Cont.)

P-channel:

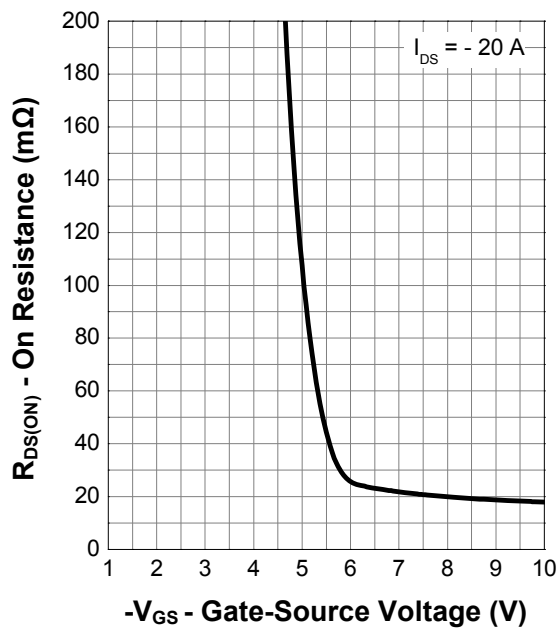
Output Characteristics



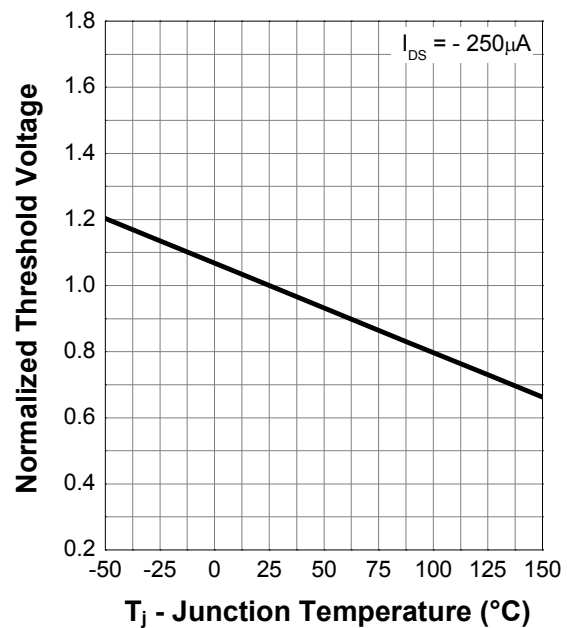
Drain-Source On Resistance

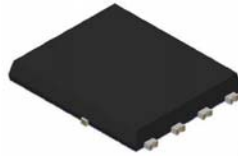


Transfer Characteristics



Normalized Threshold Voltage

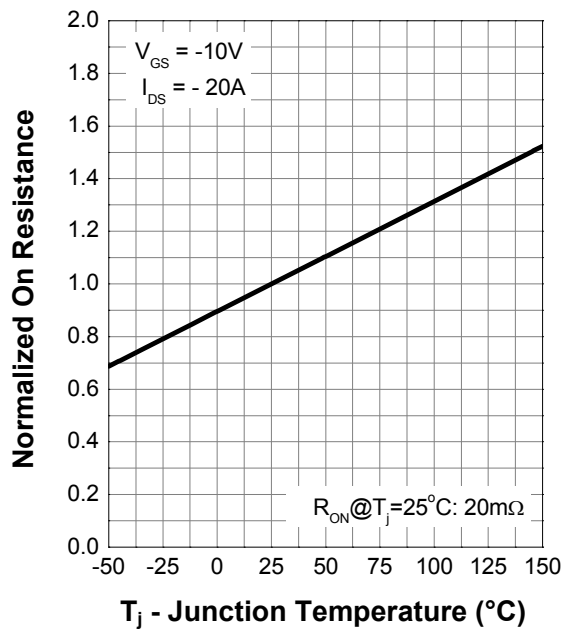




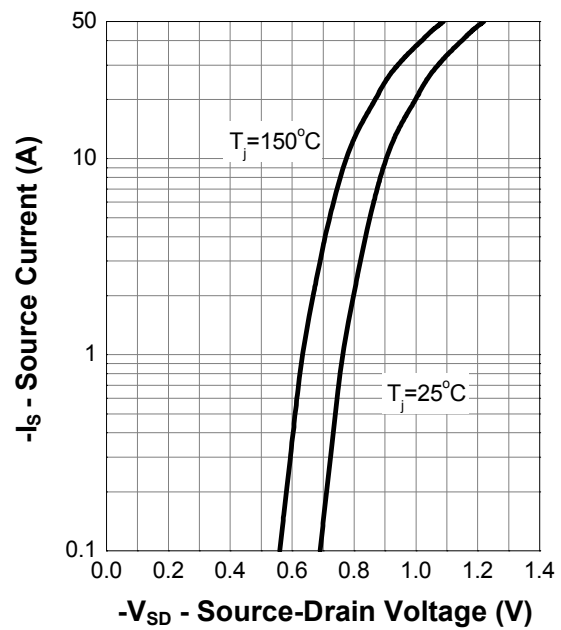
8. Typical Characteristics (Cont.)

P-channel:

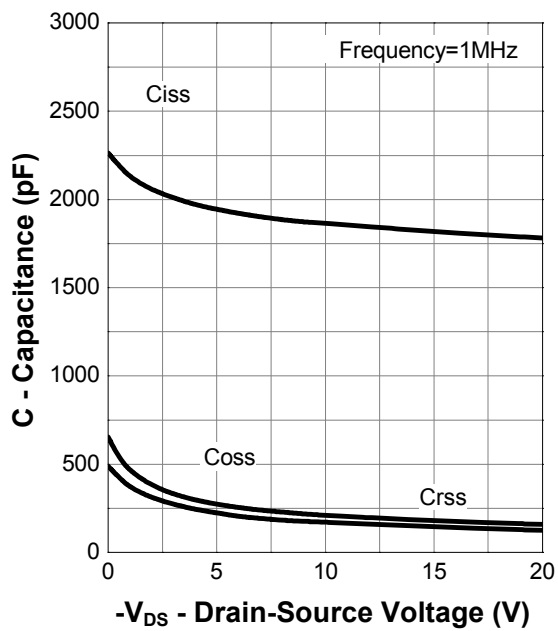
Normalized On Resistance



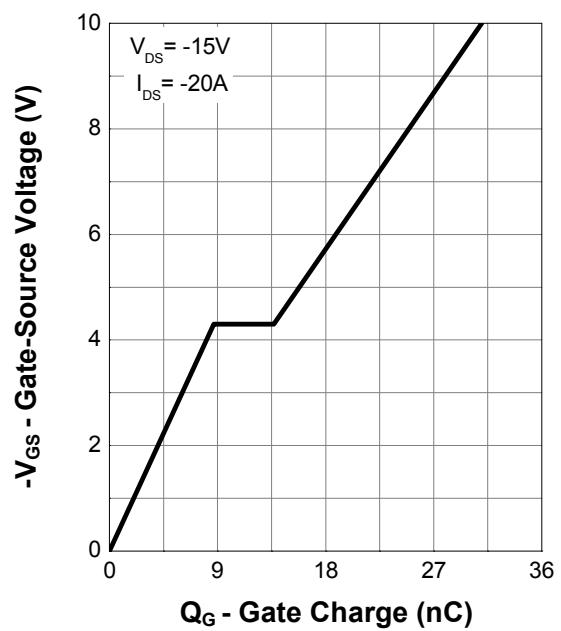
Diode Forward Current

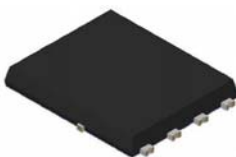


Capacitance



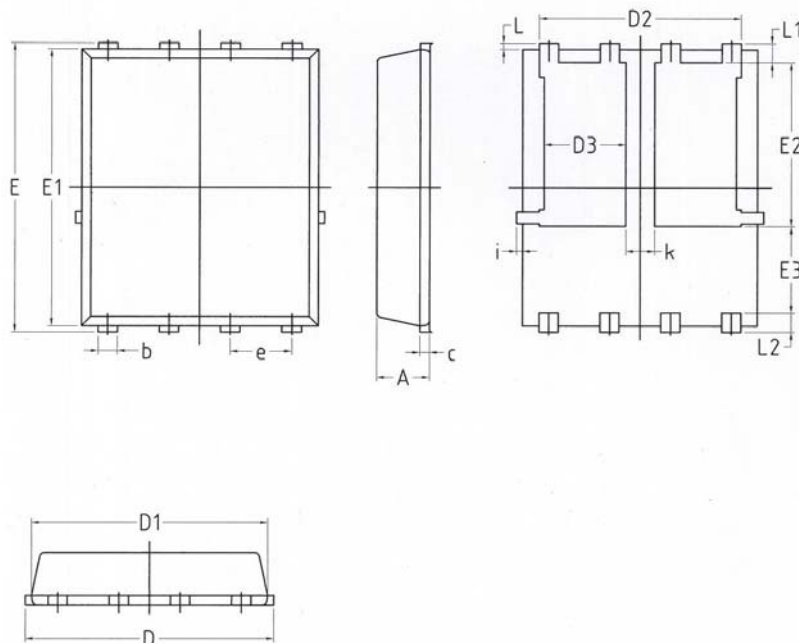
Gate Charge





9. Package Dimensions

PDFN5x6 - 8L (Dual) Package



Symbol	Dimensions In Millimeters	
	MIN.	MAX.
A	1.03	1.17
B	0.34	0.48
c	0.203 BSC	
D	4.8	5.4
D1	4.8	5.0
D2	4.11	4.31
D3	1.6	1.8
E	5.95	6.15
E1	5.65	5.85
E2	3.3	3.5
E3	1.7	-
e	1.27 BSC	
L	0.05	0.25
L1	0.38	0.5
L2	0.38	0.5
i	-	0.18
k	0.5	0.7