

FEATURES

- Small Body Outline Dimensions
- Low Body Height
- Peak Power up to 150 Watts @ 8 x 20 μs Pulse
- Low Leakage current
- Response Time is Typically < 1 ns
- ESD Rating of Class 3 (> 16 kV) per Human Body Model
- IEC61000-4-2 Level 4 ESD Protection
- IEC61000-4-4 Level 4 EFT Protection

APPLICATIONS

- Cellular phones
- Portable devices
- Digital cameras
- Power supplies

MECHANICAL DATA

- Case : Molded plastic,SOD-923
- Polarity : Shown above
- Terminals :Plated terminals, solderable per MIL-STD-750,Method 2026
- Epoxy : UL94-V0 rated flame retardant

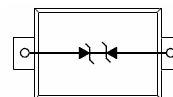
SOD-923

PACKAGE

Marking :



B = Marking Code
M = Month Code



Date Code List		
Month	Odd Year	Even Year
Jan	1	E
Feb	2	F
Mar	3	H
Apr	4	J
May	5	K
Jun	6	L
Jul	7	N
Aug	8	P
Sep	9	U
Oct	T	X
Nov	V	Y
Dec	C	Z

DISCRIPTION

The SESD9D3.3CT5 is designed to protect voltage sensitive components from ESD and transient voltage events. Excellent clamping capability, low leakage, and fast response time, make these parts ideal for ESD protection on designs where board space is at a premium.

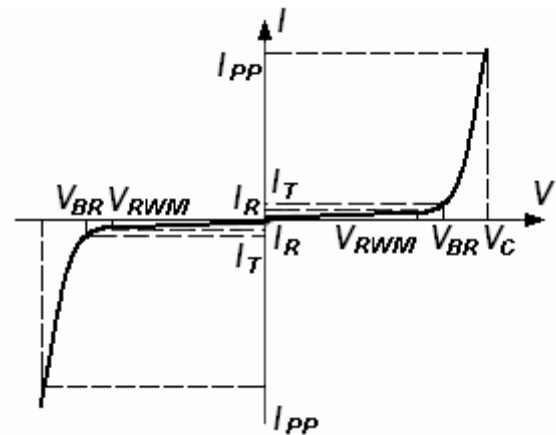
Because of its small size, it is suited for use in cellular phones, MP3 players, digital cameras and many other portable applications where board space is at a premium.

Absolute Ratings (T_{amb}=25°C)

Symbol	Parameter	Value	Units
P _{PP}	Peak Pulse Power (t _p = 8/20 μ s)	150	W
T _L	Maximum lead temperature for soldering during 10s	260	°C
T _{stg}	Storage Temperature Range	-55 to +155	°C
T _{op}	Operating Temperature Range	-40 to +125	°C
T _j	Maximum junction temperature	150	°C
	IEC61000-4-2 (ESD) air discharge	± 15	KV
	IEC61000-4-2 (ESD) contact discharge	± 8	KV
	IEC61000-4-4 (EFT)	40	A
	ESD Voltage Per Human Body Model	16	KV

Electrical Parameter

Symbol	Parameter
I_{PP}	Maximum Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_{PP}
V_{RWM}	Working Peak Reverse Voltage
I_R	Maximum Reverse Leakage Current @ V_{RWM}
I_T	Test Current
V_{BR}	Breakdown Voltage @ I_T



Electrical Characteristics

Ratings at 25°C ambient temperature unless otherwise specified. VF = 0.9V at IF = 10mA

Device	V_{RWM} (V)	I_R (uA) @ V_{RWM}	V_{BR} (V)@ I_T (Note 1)	I_T	V_C (V) @ $I_{PP}=5$ A*	V_C (V) @ Max I_{PP} *	I_{PP} (A)*	P_{PK} (W)*	C (pF)
	Max	Max	Min	mA	Typ	Max	Max	Max	Typ
SESD9D3.3CT5	3.3	1	5.0	1.0	8.4	14.1	11.2	158	25

*Surge current waveform per Figure 1.

- V_{BR} is measured with a pulse test current I_T at an ambient temperature of 25°C.

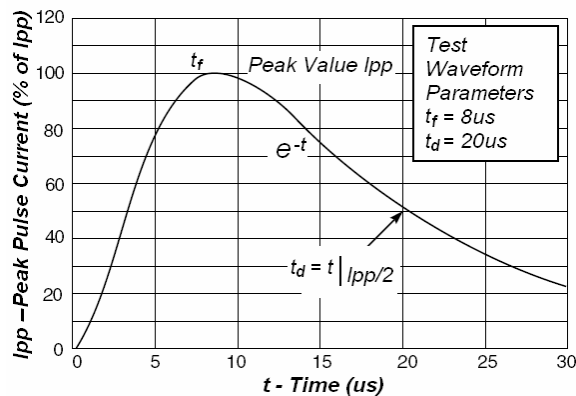


Fig1. Pulse Waveform

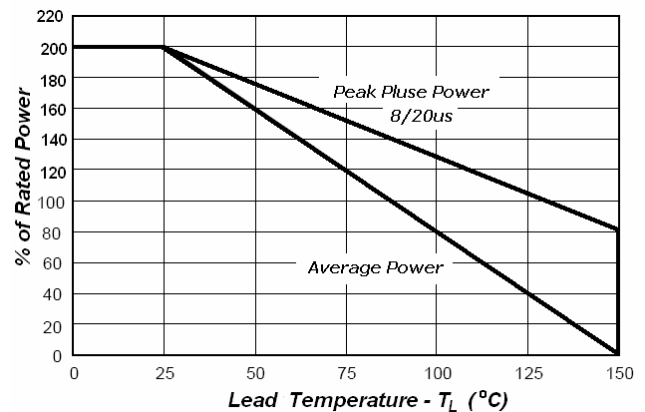
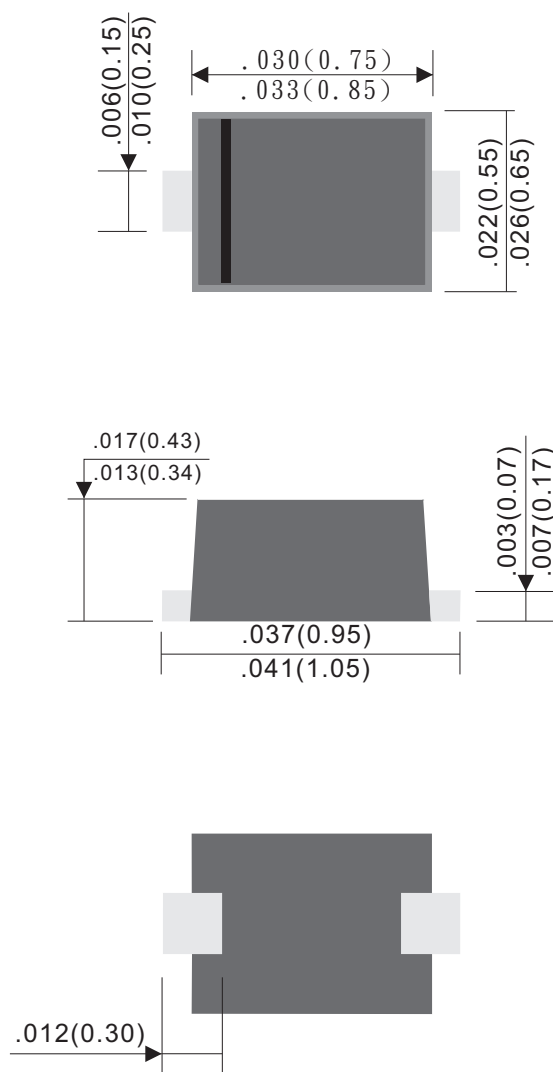


Fig2. Power Derating Curve

Outline Drawing

SOD-923



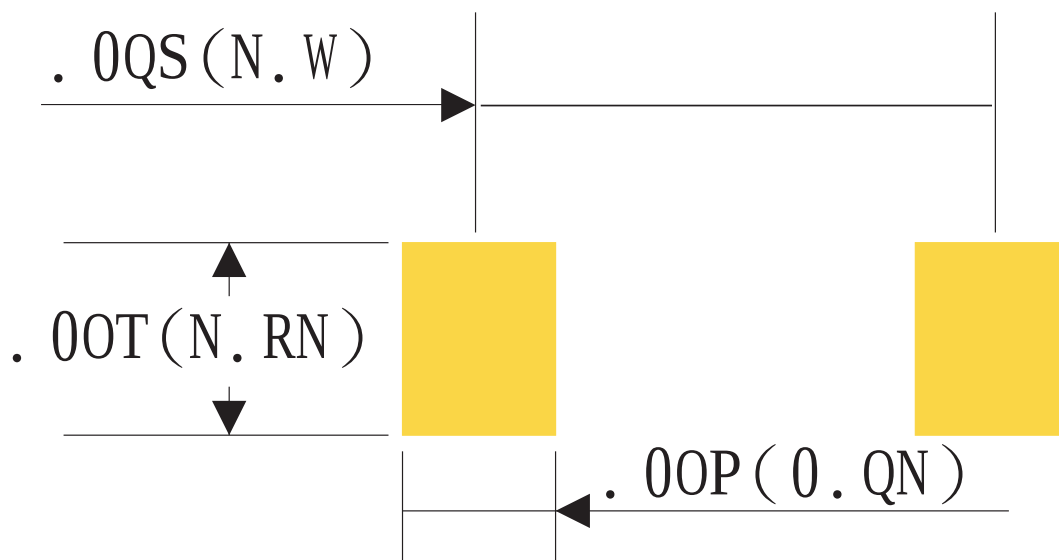
Dimensions in inches and (millimeters)

Rev.B



Suggested Soldering Pad Layout

SOD-923



Dimensions in inches and (millimeters)

RevB

Ordering Information:

Device PN	Packing
{9{5φ5oφo/φp ⁽¹⁾ ⁽²⁾ -WS	Tape&Reel: y Kpcs/Reel

Note: (1) Packing code, Tape & Reel Packing

(2) Halogen free product for packing code suffix "H"

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