

Transient Voltage Suppressors for ESD Protection

DESCRIPTION

The SESDL5V0WB is designed to protect voltage sensitive components from ESD. Excellent clamping capability, low leakage, and fast response time provide best in class protection on designs that are exposed to ESD. Because of its small size, it is suited for use in cellular phones, MP3 players, digital cameras and many other portable applications where board space is at a premium.

FEATURES

- Stand-off Voltage: 5.0 V
- Low Leakage
- Response Time is Typically < 1 ns
- ESD Rating of Class 3 (> 16 kV) Per Human Body Model
- IEC61000-4-2 Level 4 ESD Protection
- IEC61000-4-4 Level 4 EFT Protection
- This is a Pb-Free Device
- **Pb-Free package is available**
RoHS product for packing code suffix "G"
Halogen free product for packing code suffix "H"
- **Moisture Sensitivity Level 1**

WBFBP-02C

(1.0×0.6×0.5)
unit: mm



Maximum Ratings @Ta=25°C

Parameter	Symbol	Limit	Unit
IEC61000-4-2(ESD) Air Contact		±30 ±30	KV
ESD Voltage Per Human Body Model		16	KV
Per Machine Model		400	V
Total Power Dissipation on FR-5 Board (Note 1)	P_D	100	mW
Thermal Resistance Junction-to-Ambient	R _{ΘJA}	1250	°C/W
Lead Solder Temperature – Maximum (10 Second Duration)	T_L	260	°C
Junction and Storage Temperature Range	T_j, T_{stg}	-55 ~ +150	°C

Stresses exceeding maximum ratings may damage the device. Maximum Ratings are stress ratings only.

Functional operation above the recommended. Operating conditions is not implied. Extended exposure to stresses above the recommended operating conditions may affect device reliability.

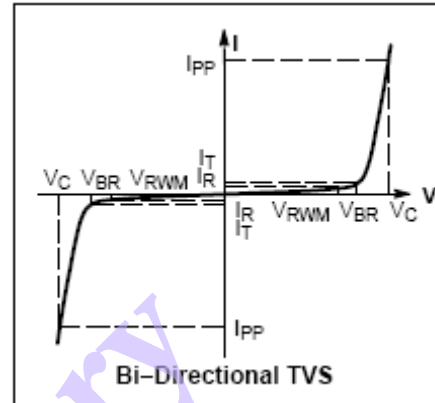
1. FR-5 = 1.0 x 0.75 x 0.62 in.



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ELECTRICAL CHARACTERISTICS (Ta = 25°C unless otherwise noted)

Symbol	Parameter
I_{PP}	Maximum Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_{PP}
V_{RWM}	Working Peak Reverse Voltage
I_R	Maximum Reverse Leakage Current @ V_{RWM}
V_{BR}	Breakdown Voltage @ I_T
I_T	Test Current
C	Max. Capacitance @ $V_R=0$ and $f=1\text{MHz}$



ELECTRICAL CHARACTERISTICS (Ta = 25°C unless otherwise noted)

Device	Device Marking	V_{RWM} (V)	I_R (μA) @ V_{RWM}	V_{BR} (V) @ I_T (Note 2)		I_T	V_C @ $I_{PP} = 5\text{ A}$	I_{PP} (A)	V_C (V) @ Max I_{PP}	C (pF)
		Max	Max	Min	Max	mA	V	Max	Max	Max
SESDL5V0WB	EB	5.0	1.0	5.8	8.8	1.0	10	11.2	12.5	30

2. V_{BR} is measured with a pulse test current I_T at an ambient temperature of 25°C.



WILLAS



SESDL5V0WB

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Ordering Information:

Device PN	Packing
SESDL5V0WB -T ⁽¹⁾ G ⁽²⁾ -WS	Tape&Reel: 10 Kpcs/Reel

Note: (1) Packing code, Tape & Reel

(2) RoHS product for packing code suffix "G" ; Halogen free product for packing code suffix "H"

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